

Form PTO 1449
(Modified)U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY DOCKET NO.
251423US2SERIAL NO. 10/816808
~~New Application~~

LIST OF REFERENCES CITED BY APPLICANT

APPLICANT
Fujio ISHIHARAFILING DATE
Herewith 4/5/04GROUP 2816

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
<u>AK</u>	AA	6,429,687 B1	08/06/02	Fujio ISHIHARA, et al.	<u>326</u>	<u>93</u>	
	AB						
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	AL						
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FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION	
					YES	NO
	AO					
	AP					
	AQ					
	AR					
	AS					
	AT					
	AU					
	AV					

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

<u>AK</u>	AW	Vadim GUTNIK, et al. "Active GHz Clock Network Using Distributed PLLs", 2000 IEEE INTERNATIONAL SOLID-STATE CIRCUITS CONFERENCE, pgs. 174-175
<u>AK</u>	AX	Nasser A. KURD, et al. "A Multigigahertz Clocking Scheme for the Pentium 4 Microprocessor", IEEE JOURNAL OF SOLID-STATE CIRCUITS, Vol. 36, No. 11, November 2001, pgs. 1647-1653
<u>AK</u>	AY	Charles E. DIKE, et al. "A Design for Digital, Dynamic Clock Deskew", 2003 SYMPOSIUM ON VLSI CIRCUITS DIGEST OF TECHNICAL PAPERS, Session 2 Advanced Clock Design, pgs. 21-24
	AZ	

☐ Additional References sheet(s) attached

Examiner

LUU

Date Considered

10/21/05

*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.